



Yi-Feng Hung (洪一峯)

Professor

Ph.D., University of California at Berkeley, U.S.A., 1991

Production Planning and Scheduling,

Computer Integrated Manufacturing,

Supply Chain Management, Heuristic Search Algorithms

E-mail: yifeng@ie.nthu.edu.tw

Yi-Feng Hung received the B.S. degree in Industrial Engineering from National Tsing Hua University, Hsinchu, Taiwan. He received the M.S. degree in Industrial Engineering and the Ph.D. in Industrial Engineering and Operations Research from the University of California at Berkeley. He has been with the Department of Industrial Engineering, National Tsing Hua University since 1993. His research interests include management of semiconductor manufacturing, supply chain management, production planning and scheduling, computer integrated manufacturing, heuristic search algorithms, and revenue management. By making significant contributions to the manufacturing management for the semiconductor companies in Science-based Industrial Park, Hsinchu, Taiwan, he received the Distinguished Industrial Collaboration Awards from the Ministry of Education twice, in 1997 and 2000, respectively. He received 2006 Outstanding I.T. Elite Award for applying mathematical programming in an apparel global logistics planning system.

Journal Papers

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16. Yi-Feng Hung and Shou-Jong Chen, 1999, "Using Queue Management for Semiconductor Manufacturing Release Control," *Journal of Management and System*, Vol.6, No.1, pp.93-110. (in Chinese)
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Conference Papers

1. Yi-Feng Hung and Ching-Bin Chang, 2000, December 13-15, "Dispatching Rules Using Flow Time Predictions for Semiconductor Wafer Fabrications," The 5th Annual International Conference on Industrial Engineering – Theory, Applications, and Practice, National Tsing Hua University, Hsinchu, Taiwan, R.O.C.
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8. Yi-Feng Hung and Robert C. Leachman, 1995, October, “The Relationship Between Cycle Time and Safety Stock for Semiconductor Manufacturing,” *IEEE Transactions/1995 Semiconductor Manufacturing Workshop*, Taiwan.
9. Yi-Feng Hung, May 18-20, 2007, “Order assignments for a global apparel production network”, *The 2nd International Conference on Operations and Supply Chain Management (OSCM) 2007*, Bangkok, Thailand.
10. Yi-Feng Hung, 1990, August, “Analysis of Reduced Factory Models to Set Parameters of the Company-Wide Production Planning Model,” *Fifth Annual SRC/DAPRA CIM-IC Workshop*, U.S.A.
11. Yi-Feng Hung and S. Adiga, 1988, August, “Knowledge-Based Simulation System for A wafer Fabrication Shop,” *ASME 1988 Computer in Engineering Conference*, U.S.A.